ABHISHEK KUMAR MOROPANTHULU

Java Full Stack Developer

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Software Developer with a foundation in both developing and in testing, backend fundamentals including Core Java, JDBC, Servlets, database technology, manual testing, API Testing and continuously learning to compete with evolving technologies and AI while building better software solutions with confidence in my skills.

EDUCATION

Bachelor of Technology (ECE), *Gates Institute of Technology Gooty* 2022 – 2026
CGPA: 8.3 Anantapur, India

TECHNICAL SKILLS

PROGRAMMING
CORE JAVA - OOP, Exception Handling, Arrays, Strings, Wrapper Class, Packages, File Handling, Multithreading, Collection (List, Set, Queue, Map).
ADV JAVA - JDBC, SERVLETS, MySQL Connection.

DATABASE
ORACLE SQL - DQL Statement, Functions, Clauses, Subquery, Joins, DDL, DML, TCL, DCL Statements, Normalization, Indexes.

TESTING
MANUAL TESTING - SDLC, STLC, White Box Testing, Black Box Testing, Severity & Priority, Test Cases, Test Plan, Defects, Test Case Design, Agile Process.
JSON - Objects, Arrays, Data Types, Nested Objects, Serialization, Deserialization, API Request/Response.
Selenium - Selenium WebDriver, Locators, XPath, CSS Selectors, Web Element Handling, Browser Navigation, Dropdowns, Alerts, Frames, Windows, Waits, Mouse & Keyboard Actions, Screenshots.
API TESTING - Postman, REST APIs, HTTP Methods, Status Codes, Request & Response Validation.

PROJECTS

Library Management System , *Technologies Used: JDBC • SERVLET • HTML • CSS • MYSQL*
A web-based Library Management System developed using Java Servlets, JDBC, MySQL, HTML, CSS, and JavaScript that demonstrates user authentication, database integration, session management, and CRUD operations.

Employee Payroll System , *Technologies Used: Java • Eclipse IDE • Git • GitHub*
A menu-driven Employee Payroll System developed using Core Java that demonstrates Object-Oriented Programming (OOP) concepts such as inheritance, method overriding, constructors, runtime polymorphism, and dynamic user input.

Design & Verification of APB Memory Using SystemVerilog,
Technologies Used: Verilog HDL • SystemVerilog • UVM • AMBA APB Protocol • FSM • Simulation
- Developed an APB-based memory interface for low-speed peripheral communication in SoC designs using Verilog HDL.
- Implemented APB protocol transactions, memory read/write operations, and FSM control logic.

Pinwheel Web Application, *Manual Testing*
• Explored the Pinwheel web application to identify software defects across different modules. • Identified Defects, including UI, functional, usability, and navigation issues. • Classified defects based on Severity and Priority.

MINI PROJECTS

Frontend Mentor Challenges, Instagram Clone
- Completed targeted micro-challenges from Frontend Mentor to refine CSS semantic styling and mobile-first design principles.
- Built a fully responsive Instagram Clone to master intricate layout positioning and flexbox models.

EXPERIENCE & INTERNSHIPS

Java Development, *Q-Skill* 08/2026 – Present
Working on Java development tasks involving application logic, problem-solving, and implementation of Java-based solutions in a practical project environment. Remote

Front-End Development Intern, *Q-Skill*
Developed production-ready UI components using React.js and Tailwind CSS in a commercial development environment.

MERN-Stack Development Intern, *Web-stack Academy*
Selected for a MERN Stack internship to work on Homely Hub, a property booking platform, gaining planned hands-on exposure to MongoDB, Express.js, React.js, Node.js, and full-stack web development.

CERTIFICATIONS

HTML	**Web Development**	**SQL**	**Git**
University of Michigan	Microsoft	Microsoft	Atlassian

DECLARATION

I hereby declare that the information provided above is true and accurate to the best of my knowledge.